ADDRESS BUS
CHANGE OVER BOTH PATCHES FOR AT28HC256 EPROM INSTEAD OF OTP
ADDRESS BUS
TIMING LOGIC
Z80 CPU
Z84C0010PEG
NOP ENFORCER
OTP (OR EPROM) IN PLCC32 SOCKET
STATIC RAM
CHARACTER BYTE LATCH
PIXEL SHIFT REGISTER
EDGE CONNECTOR
8X 1N4148WS
KEYBOARD ROW
KEYBOARD COLUMN
KEYBOARD LEDS
CPU CONTROL BUS
SYSTEM BUS
DATA BUS
CPU CONTROL BUS
TO BATTERY CR2032
SRAM RETENTION CIRCUIT
CHARACTER ROW COUNTER
B/W PIXEL FILL
INVERT/NORMAL VID
KEYBOARD IORD DRIVER
TURBO LOGIC
TURBO ON LED
PULLUPS SO IORD ON FLOATING BUS READS $FF. FOR PROPER PRINTER DETECTION AND SUCH.
WAIT SYNC LOGIC
GPIO BUS
LCD OPTION
SOUND INTERFACE
PLACE JUMPER FOR NTSC (60HZ)
HSYNC AND FRONTPORCH GENERATION LOGIC
PORCH = FIRST 16 COUNTS
HSYNC = LAST 16 COUNTS
VSYNC LOGIC
I/O DECODER
NMI LOGIC
OUTPUT LATCH FOR EXTRA GPIO (8 OUTPUT BITS)
COMBINED LCD SOUND & GPIO HEADER
INVERT AND SHIFT/LOAD LOGIC
NOP GENERATOR
MEMORY DECODER
ALTERNATIVE WAIT SYNC LOGIC WITH 10 % SPEEDUP ON/OFF SWITCH
COMPATIBLE - FAST SWITCH
SLIDE SWITCH SW1
T6 & T7 ACT LIKE LATCHING THYRISTOR
VIDEO OUT
RCA
TO MICROPHONE
FROM SPEAKER
JOYSTICK PORT
JOYSTICK TO KEYS 5, 6, 7, 8 & 0 INTERFACE
5V POWER INPUT
SYSTEM BUS
ADDRESS BUS
CPU CONTROL BUS
SYSTEM BUS
CPU CONTROL BUS

REV: 1.1	DATE: 1-7-2014	ENG: MAHJONGG
PROJECT: ZX14		
COMPANY: REVSPACE ADDRESS: SPOORLAAN 5D, 2495 AL CITY DEN HAAG COUNTRY: NETHERLANDS		
INITIAL 12-2-2014	PAGE: 1	OF: 1